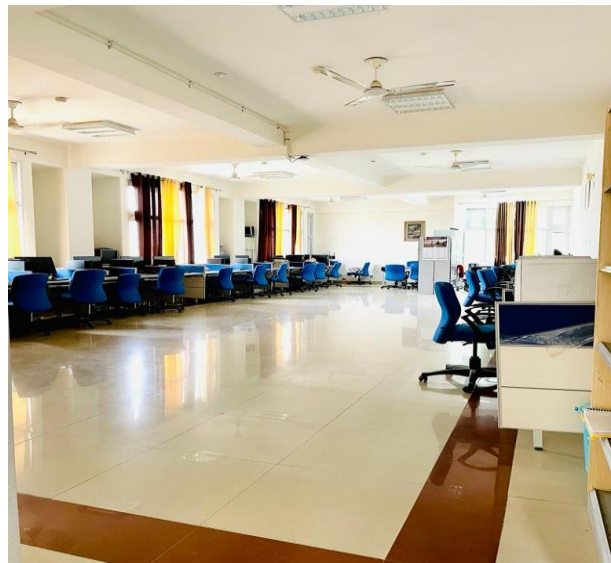


VLSI AND NANO LAB

The VLSI and Nano Lab is to enable the students to learn Hardware Descriptive Language(Verilog/VHDL), to learn the fundamental principles of VLSI circuit design in digital and analog domain, to familiarize fusing of logical modules on FPGAs, and to provide hands on design experience with professional design (EDA) platforms.



OIC Name: Dr. Ashwani Kumar Rana

Technician: Mr Shiv Dyal

Equipment Details

Hardware/Software	Qty
High end PC workstation.	03
TFT LCD Monitor.	03
PC Workstation	07
TFT LCD Monitor	07
Spartan 3E Kits	15
Vertex Pro –Board	05

DI/O Boards	05
AI/O Boards	05
V DEC Boards	05
MEMS Pro Software	05 Node Locked License
Vertex-5 Kit	01
Matlab 7.5 Release 2007	01 of 10 Users
Workstation Intel Core2DUO with TFT Monitor.	10
T-CAD academic/ University bundle (Software)	01 of 10 Users
T-CAD Server-Re-Hosting	5 User Professional Version(2D&3D)5 user basic Version +1 User Practicle
DFT Based Simulator for Nano technology simulation (Software)	01